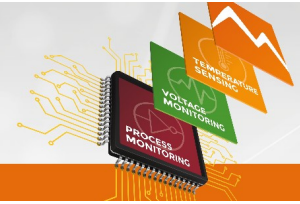


Role: Senior Analog IC Design Engineer

Reporting to: Engineer Manager

Based: Flexible

In-Chip Monitoring
Subsystem Solutions



About the Company:

Moortec Semiconductor is no ordinary company. We are looking for talented and enthusiastic self-starters with inquiring minds who are willing to support our cutting edge work in semiconductor IP design. We licence our in-chip monitoring products to a range of Blue Chip companies around the globe. From modest beginnings in 2005, we have now established ourselves as market-leaders and a forward-thinking global company and an industry employer of choice.

Our working environment is welcoming and friendly and you'll be surrounded by some of the brightest minds in the industry and world-class Engineers. For some of our roles it doesn't matter if you're not from an engineering background but it's always important you can share our company values of; **Expertise, Teamwork, Integrity and Balance.**

Employees are offered a challenging and fulfilling career path, where you will be encouraged to let your ideas flourish at every level and be supported in your development to fulfil your potential.

We also offer an excellent salary with a fantastic benefits package, flexible working, regular social events, modern offices located within the UK and Poland.

Unless otherwise specified the location of the position is flexible and can be located at Plymouth UK, Bristol Temple Meads UK or Gdansk Poland

About The Role:

As part of the expanding engineering team, this role will involve designing, simulating and verifying full custom analog and mixed-signal circuits and IP blocks, on some of the most advanced semiconductor nodes available.

Qualifications, Skills & Experience:

Essential Skills:

- 5+ years of experience in analog/mixed-signal IC design.
- Proven track record of delivering tasks on schedule.
- Degree (or equivalent qualification) in electronic engineering.
- Awareness of full custom layout techniques.
- Good communication and interpersonal skills.

Desired:

- Knowledge of Cadence tools is highly desirable.
- Familiarity with mixed—signal simulation flows

Continuous Professional Development:

With the support and direction of the company, we actively encourage and expect all team members to continually develop the technical skills, competencies and experience in order to meet the current and future needs of the business.

Responsibilities and Duties:

- Design, simulate and verify analog block designs.
- Involvement in full analog IC design flow including specification, feasibility, design, simulation, and documentation.
- Working with physical design engineers to ensure design objectives are met.
- Analysis of silicon data.
- Provide engineering expert support to customers.
- Provide technical support to junior engineers.

Salary & Benefits:

- Competitive salary
- Supportive career development
- Flexible working
- Company events
- Regular pay reviews
- Company bonus scheme
- 28 days holiday + bank holidays
- Company pension scheme
- Life assurance cover
- HealthShield cash plan (UK)
- Gym Membership (UK)
- Access to company share scheme

Due to local legislation regional adjustments may apply for positions outside of the UK